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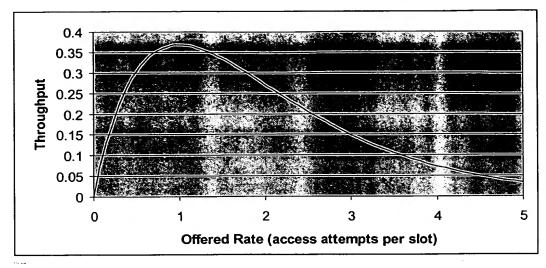


Figure 1. Throughput for a Slotted ALOHA Channel

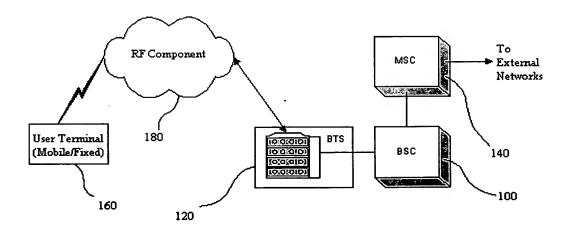


Figure 2. BSC Interfaces

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Figure 3 - Level 1 Congestion Control Transition Matrix

	1	2	4	7
1 0.23047	2 0.1869	981 0.09	3491	0
2 0.48047	2 0.2804	972 0.14	0236	.,,
 4 0.74023	604804	172 0.28	0472	0
7 0.82632	4 0.6530	648 0.48	0472	. 0

Figure 4 - Comparison of Results

Congestion Control Implemented	Input Rate	Thoughput	Mem Delay (ms)	Blocking Probability
NO	0.75	0.182991	2853.925636	0.756001
YES	0.75	0.534169	1013.23703	0.287466

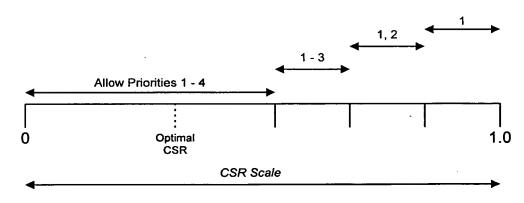


Figure 5. Level 2 Congestion Control

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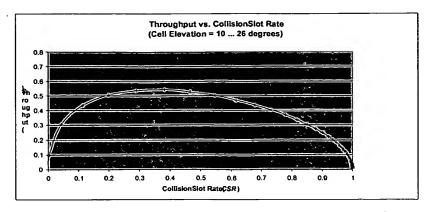


Figure 6 - CSR versus Throughput S

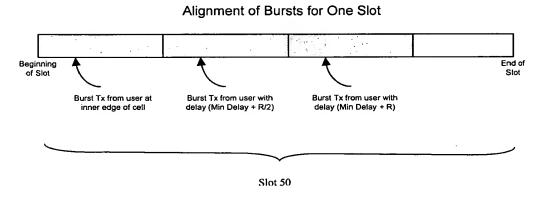


Figure 7 - Burst Alignment within Slotted ALOHA RACH

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Figure 8 Congestion Control Process

